

PPPPPPPP	AAAAAA	TTTTTTTT	SSSSSSSS	PPPPPPPP	AAAAAA
PPPPPPPP	AAAAAA	TTTTTTTT	SSSSSSSS	PPPPPPPP	AAAAAA
PP PP	AA AA	TT	SS	PP PP	AA AA
PP PP	AA AA	TT	SS	PP PP	AA AA
PP PP	AA AA	TT	SS	PP PP	AA AA
PP PP	AA AA	TT	SS	PP PP	AA AA
PPPPPPPP	AA AA	TT	SSSSSS	PPPPPPPP	AA AA
PPPPPPPP	AA AA	TT	SSSSSS	PPPPPPPP	AA AA
PP	AAAAAAAAAA	TT	SS	PP	AAAAAAAAAA
PP	AAAAAAAAAA	TT	SS	PP	AAAAAAAAAA
PP	AA AA	TT	SS	PP	AA AA
PP	AA AA	TT	SS	PP	AA AA
PP	AA AA	TT	SSSSSSSS	PP	AA AA
PP	AA AA	TT	SSSSSSSS	PP	AA AA
PP	AA AA	TT	SSSSSSSS	PP	AA AA
PP	AA AA	TT	SSSSSSSS	PP	AA AA

LL		SSSSSSSS
LL		SSSSSSSS
LL		SS
LLLLLLLL		SSSSSSSS
LLLLLLLL		SSSSSSSS

```
1 0001 0 MODULE PATSPA (%IF %VARIANT EQL 1
2 0002 0           %THEN
3 0003 0               ADDRESSING_MODE (EXTERNAL = LONG_RELATIVE,
4 0004 0               NONEXTERNAL = LONG_RELATIVE),
5 0005 0
6 0006 0           %FI
7 0007 0           IDENT = 'V04-000'
8 0008 1 BEGIN
9 0009 1
10 0010 1
11 0011 1 ****
12 0012 1   *
13 0013 1   * COPYRIGHT (c) 1978, 1980, 1982, 1984 BY
14 0014 1   * DIGITAL EQUIPMENT CORPORATION, MAYNARD, MASSACHUSETTS.
15 0015 1   * ALL RIGHTS RESERVED.
16 0016 1   *
17 0017 1   * THIS SOFTWARE IS FURNISHED UNDER A LICENSE AND MAY BE USED AND COPIED
18 0018 1   * ONLY IN ACCORDANCE WITH THE TERMS OF SUCH LICENSE AND WITH THE
19 0019 1   * INCLUSION OF THE ABOVE COPYRIGHT NOTICE. THIS SOFTWARE OR ANY OTHER
20 0020 1   * COPIES THEREOF MAY NOT BE PROVIDED OR OTHERWISE MADE AVAILABLE TO ANY
21 0021 1   * OTHER PERSON. NO TITLE TO AND OWNERSHIP OF THE SOFTWARE IS HEREBY
22 0022 1   * TRANSFERRED.
23 0023 1   *
24 0024 1   * THE INFORMATION IN THIS SOFTWARE IS SUBJECT TO CHANGE WITHOUT NOTICE
25 0025 1   * AND SHOULD NOT BE CONSTRUED AS A COMMITMENT BY DIGITAL EQUIPMENT
26 0026 1   * CORPORATION.
27 0027 1   *
28 0028 1   * DIGITAL ASSUMES NO RESPONSIBILITY FOR THE USE OR RELIABILITY OF ITS
29 0029 1   * SOFTWARE ON EQUIPMENT WHICH IS NOT SUPPLIED BY DIGITAL.
30 0030 1   *
31 0031 1   *
32 0032 1 ****
33 0033 1
34 0034 1
35 0035 1 ++
36 0036 1   FACILITY: PATCH
37 0037 1
38 0038 1   ABSTRACT: THIS ROUTINE HANDLES FREE PATCH AREA, ALIGNMENT, ALLOCATION, AND EXPANSION.
39 0039 1
40 0040 1   ENVIRONMENT: VAX/VMS
41 0041 1
42 0042 1   AUTHOR: K.D. MORSE . CREATION DATE: 17-NOV-77
43 0043 1
44 0044 1   MODIFIED BY:
45 0045 1
46 0046 1   V03-003 MTR0025 Mike Rhodes 11-Aug-1983
47 0047 1   Modify routine PAT$EXP AREA to signal an ERROR (severity)
48 0048 1   message when an expansion request is made while patching
49 0049 1   a file in ABSOLUTE mode. This will cause the current
50 0050 1   command to be aborted and the user is returned back to the
51 0051 1   PATCH command prompt. Files may NOT be expanded in absolute
52 0052 1   mode, as could result from a command like:
53 0053 1   PATCH> REPLACE/INST 20='movl r0,r1'
54 0054 1   NEW> 'movl r0,r1'
55 0055 1   NEW> 'bneq 200'
56 0056 1   NEW> EXIT
57 0057 1
```

58 0058 1 : V03-002 MTR0016 Mike Rhodes 03-Nov-1982
59 0059 1 : Modify PAT\$BUILD_ISE to accept one additional argument which
60 0060 1 : is the address to be modified. This address is used for INSERT
61 0061 1 : and REPLACE commands when patching protected shareable images.
62 0062 1 : The attributes of the image section which contains the address
63 0063 1 : being modified will be propagated to the newly created default
64 0064 1 : patch area.
65 0065 1 :
66 0066 1 : V03-001 MTR0007 Mike Rhodes 14-Jun-1982
67 0067 1 : Use shared system messages. Affected modules include:
68 0068 1 : DYNMEM.B32, PATBAS.B32, PATCMD.B32, PATIHD.B32, PATINT.B32,
69 0069 1 : PATIO.B32, PATMAI.B32, PATMSG.MSG, PATWRT.B32, and PATSPA.B32.
70 0070 1 :
71 0071 1 : The shared messages are defined by DYNMEM.B32's invocation of
72 0072 1 : SHRMSG.REQ and we simply link against these symbols. They are
73 0073 1 : declared as external literals below.
74 0074 1 :
75 0075 1 : V03-000 MTR0001 Mike Rhodes 15-Mar-1982
76 0076 1 : Modify routine PAT\$EXP_AREA to allow PIC SHR images to be
77 0077 1 : patched using default patch area which may be expanded as
78 0078 1 : needed. Also, removed the old 50% growth area logic which
79 0079 1 : has been made obsolete by the above change.
80 0080 1 :
81 0081 1 : V02-008 MTR0001 Mike Rhodes 15-Sep-1981
82 0082 1 : Modify routine PAT\$BUILD_ISE. The location algorithm
83 0083 1 : for placing the PATCH ISE/ISD pair in the ISE list is
84 0084 1 : as follows:
85 0085 1 : The PATCH ISE/ISD pair are located in the ISE list
86 0086 1 : FOLLOWING the last "Normal" ISD and PRECEDING the
87 0087 1 : first Non-Based Global or Stack ISDs.
88 0088 1 :
89 0089 1 : Included in the modification is the definition of two new
90 0090 1 : variables, PREV_ISE_PTR - Pointer to Previous ISE, and
91 0091 1 : TEMP - Holds the FLINK from the previous
92 0092 1 : ISE till its put into the new ISE.
93 0093 1 :
94 0094 1 : V02-007 PCG0001 Peter George 02-FEB-1981
95 0095 1 : Add require statement for LIB\$:PATDEF.REQ
96 0096 1 :
97 0097 1 : V0206 CNH0038 Chris Hume 4-Oct-1980 16:00
98 0098 1 : Last Cluster will now remain set when new Patch Area is added.
99 0099 1 : Patch Area will be allocated at a distance one half the size of
100 0100 1 : the Last Cluster (beyond its end).
101 0101 1 :
102 0102 1 : V0105 CNH0023 Chris Hume 16-Nov-1979 14:00
103 0103 1 : Turn off ISD\$V LASTCLU for all ISD's when PATCH Area is added
104 0104 1 : to an image. Also unrecognized languages will now be processed
105 0105 1 : as though they were MACRO. (PATBLD.B32 V0117, PATMAI.B32 V0228)
106 0106 1 :
107 0107 1 : V0104 CNH0015 Chris Hume 27-Sep-1979 11:30
108 0108 1 : Changed GBLWARN message from a warning to an informational.
109 0109 1 : Added section name to the signal. Added EXPSHRPAT error.
110 0110 1 : (PATMAI.B32 V0225, PATMSG.MDL V0203, PATARI.B32 V0112)
111 0111 1 :
112 0112 1 : MODIFICATIONS:
113 0113 1 :
114 0114 1 : NO DATE PROGRAMMER PURPOSE

PATSPA
V04-000

N 13
16-Sep-1984 00:57:14 VAX-11 Bliss-32 V4.0-742
14-Sep-1984 12:52:47 DISK\$VMSMASTER:[PATCH.SRC]PATSPA.B32;1 Page 3
(1)

115	0115	1	1	--	----	-----	-----
116	0116	1	1	--	----	-----	-----
117	0117	1	1	01	07-MAR-78	K.D. MORSE	ADD ROUTINES PAT\$ADD PAL.
118	0118	1	1	02	25-APR-78	K.D. MORSE	CONVERT TO NATIVE COMPILER.
119	0119	1	1	03	13-JUN-78	K.D. MORSE	ADD FAO COUNTS TO SIGNALS.
120	0120	1	1	--	----	-----	-----
121	0121	1	1	--	----	-----	-----

PA1
VO4

123 0122 1 ! TABLE OF CONTENTS:
124 0123 1 !
125 0124 1 !
126 0125 1 ! FORWARD ROUTINE
127 0126 1 ! PAT\$ALIGN_CMD : NOVALUE,
128 0127 1 ! PAT\$BUILD_ISE : NOVALUE,
129 0128 1 ! PAT\$EXP_AREA : NOVALUE,
130 0129 1 ! PAT\$ADD_PAL : NOVALUE;
131 0130 1 !
132 0131 1 !
133 0132 1 !
134 0133 1 ! INCLUDE FILES:
135 0134 1 !
136 0135 1 LIBRARY 'SYSS\$LIBRARY:LIB.L32';
137 0136 1 REQUIRE 'SRC\$:PATPCT.REQ';
138 0176 1 REQUIRE 'SRC\$:PATGEN.REQ';
139 0398 1 REQUIRE 'SRC\$:VXSMAC.REQ';
140 0463 1 REQUIRE 'SRC\$:PREFIX.REQ';
141 0651 1 REQUIRE 'SRC\$:PATPRE.REQ';
142 0814 1 REQUIRE 'LIB\$:PATDEF.REQ';
143 0868 1 REQUIRE 'LIB\$:PATMSG.REQ';
144 1042 1 REQUIRE 'SRC\$:BSTRUC.REQ';
145 1118 1 REQUIRE 'SRC\$:LISTEL.REQ';
146 1160 1 REQUIRE 'SRC\$:DLLNAM.REQ';
147 1218 1 REQUIRE 'SRC\$:SYSSER.REQ';

! Executes align command
! Builds an image section descriptor
! Expands patch area
! Adds entry to PAL

! Defines System structure definitions
! Defines PSECTs
! Defines context bits
! Defines common macros
! Defines structure macros
! Defines PATCH structures
! Defines literals
! Defines error message codes
! Defines basic structures
! Defines list structures
! Defines symbol table entry offsets
! Defines FAO output macros

PATSPA
V04-000

C 14
16-Sep-1984 00:57:14 VAX-11 Bliss-32 V4.0-742
15-Sep-1984 22:50:49 \$255\$DUA28:[PATCH.SRC]SYSSER.REQ;1 Page 5
(1)

: R1250 1 SWITCHES LIST (SOURCE);
: R1251 1
: R1252 1 EXTERNAL ROUTINE
: R1253 1 PAT\$fao_out;
: R1254 1 ! formats a line and outputs to the terminal

PAT
V04

```

148 1300 1 MACROS:
149 1301 1
150 1302 1
151 1303 1
152 1304 1
153 1305 1 EQUATED SYMBOLS:
154 1306 1
155 1307 1
156 1308 1
157 1309 1 OWN STORAGE:
158 1310 1
159 1311 1 OWN
160 1312 1 PAT_AREA_NAME : VECTOR[4,BYTE] INITIAL(%ASCIC 'PAA'), ! Next patch area name
161 1313 1 PA_NAME_DSC : VECTOR[2,WORD] INITIAL(A_LONGWORD-A_BYTE, CH$PTR(PAT_AREA_NAME, 1)); ! String descriptor
162 1314 1
163 1315 1
164 1316 1 EXTERNAL REFERENCES:
165 1317 1
166 1318 1 EXTERNAL
167 1319 1 PAT$GL_PAL_LHD : REF BLOCK[,BYTE], Patch area listhead
168 1320 1 PAT$GL_ERRCODE, Error code
169 1321 1 PAT$GL_CONTEXT : BITVECTOR, Context bits
170 1322 1 PAT$GL_FLAGS : BITVECTOR [32], CLI flags.
171 1323 1 PAT$GL_IMGHDR : REF BLOCK[,BYTE], Pointer to image header
172 1324 1 PAT$GL_PATAREA : REF BLOCK[,BYTE], Free patch area descriptor pointer
173 1325 1 PAT$GL_IHPPTR : REF BLOCK[,BYTE], Pointer to patch area of image header
174 1326 1 PAT$GL_ISELHD, ISE List Head
175 1327 1 PAT$GL_ISETAIL : REF BLOCK[,BYTE], Pointer to tail of ISE table
176 1328 1 PAT$GL_NEWVPNMX, Max VPN of image sections in new image
177 1329 1 PAT$GL_NEWVBNMX, Max VBN of image sections in new image
178 1330 1 PAT$GL_IMGBLKS, Number of blocks in new image
179 1331 1 PAT$GL_ISVADDR : VECTOR[,LONG], Addresses of last image section mapped
180 1332 1 PAT$GL_HEAD_LST, Head of command argument list
181 1333 1 PAT$GL_SYMTBPT, Pointer to current default symbol table
182 1334 1 PAT$GL_SYMHEAD; Pointer to listhead entry for user-defined
183 1335 1
184 1336 1 EXTERNAL ROUTINE
185 1337 1 PAT$ALLOCBLK : NOVALUE, Allocates free storage
186 1338 1 PAT$CREMAP : NOVALUE, Creates and maps image sections
187 1339 1 PAT$DEFINE_SYM : NOVALUE, Defines a symbol
188 1340 1 PAT$FIND_SYM, Find symbol definition
189 1341 1 PAT$FREEZ, Allocates and zeros free storage
190 1342 1 PAT$MAP_ADDR : NOVALUE; Maps an image address
191 1343 1
192 1344 1 EXTERNAL LITERAL
193 1345 1
194 1346 1 Define shared message references. (resolved @ link time)
195 1347 1
196 1348 1 PAT$CLOSEIN, Error closing input file.
197 1349 1 PAT$CLOSEOUT, Error closing output file.
198 1350 1 PAT$OPENIN, Error opening input file.
199 1351 1 PAT$OPENOUT, Error opening output file.
200 1352 1 PAT$READERR, Error reading from file.
201 1353 1 PAT$SYSERROR, System Service error.
202 1354 1 PAT$WRITEERR, Error writing to file.
203 1355 1

```

205 1356 1 GLOBAL ROUTINE PAT\$ALIGN_CMD : NOVALUE = ! Performs align commands
206 1357 1
207 1358 1 !++
208 1359 1 | FUNCTIONAL DESCRIPTION:
209 1360 1 |
210 1361 1 | This routine aligns a free patch area to the requested boundary,
211 1362 1 | word, longword, quadword, or page. The patch area bytes between the
212 1363 1 | old address and the rounded address are lost for patching purposes.
213 1364 1 | The symbol name provided in the command is entered into the symbol list
214 1365 1 | with a value of the patch area address. If the free patch area is not
215 1366 1 | large enough to be rounded to the appropriate boundary, an error is
216 1367 1 | SIGNALed and the alignment does not take place. The free area
217 1368 1 | descriptor remains unchanged.
218 1369 1 |
219 1370 1 | If the symbol name was previously defined, a message is produced and
220 1371 1 | the name is redefined to the new patch area address.
221 1372 1 |
222 1373 1 | Aligning the patch area to a byte boundary will merely cause the
223 1374 1 | symbol to be defined as the next free byte of patch area.
224 1375 1 |
225 1376 1 | FORMAL PARAMETERS:
226 1377 1 |
227 1378 1 | none
228 1379 1 |
229 1380 1 | IMPLICIT INPUTS:
230 1381 1 |
231 1382 1 | The symbol name descriptor is set up by the parser.
232 1383 1 | The context bits have already been set up for the command.
233 1384 1 | The user-defined symbol table has been initialized as has the
234 1385 1 | free memory handler.
235 1386 1 |
236 1387 1 | IMPLICIT OUTPUTS:
237 1388 1 |
238 1389 1 | none
239 1390 1 |
240 1391 1 | ROUTINE VALUE:
241 1392 1 |
242 1393 1 | none
243 1394 1 |
244 1395 1 | COMPLETION CODES:
245 1396 1 |
246 1397 1 | none
247 1398 1 |
248 1399 1 | SIDE EFFECTS:
249 1400 1 |
250 1401 1 | The default patch area is aligned to the appropriate boundary.
251 1402 1 | If there is not enough patch area to align, a new patch area is
252 1403 1 | created.
253 1404 1 |
254 1405 1 !--
255 1406 1
256 1407 2 BEGIN
257 1408 2
258 1409 2 LITERAL ONE_BLOCK = 1; ! Number of blocks to expand patch area by
259 1410 2
260 1411 2 LOCAL
261 1412 2

```
262      1413 2      TEMP_SYMTB,  
263      1414 2      ALIGN_FACTOR,  
264      1415 2      DESC_PTR : REF_BLOCK[,BYTE],  
265      1416 2      SYM_ENTRY_PTR,  
266      1417 2      PATCH_AREA_ADR,  
267      1418 2      PATCH_AREA_SIZ:  
268  
269      1420 2      !++  
270      1421 2      ! Output current patch area statistics before alignment.  
271      1422 2      !--  
272      1423 2      $FAO_TT_OUT('old patch area size: !XL', .PAT$GL_PATAREA[DSC$W_LENGTH]);  
273      1424 2      $FAO_TT_OUT('old patch area address: !XL', .PAT$GL_PATAREA[DSC$A_POINTER]);  
274  
275      1426 2      !++  
276      1427 2      ! Check for conflicting patch area requests and set up alignment factor.  
277      1428 2      ! The alignment factor is set to the number of bytes in a longword, word,  
278      1429 2      ! byte, page, or quadword.  
279      1430 2      IF .PAT$GL_CONTEXT[ALIGN_BYTE]  
280      1431 2      THEN  
281          ALIGN_FACTOR = A_BYTE;  
282      1433 2      IF .PAT$GL_CONTEXT[ALIGN_WORD]  
283      1434 2      THEN  
284          ALIGN_FACTOR = A_WORD;  
285      1436 2      IF .PAT$GL_CONTEXT[ALIGN_LONG]  
286      1437 2      THEN  
287          ALIGN_FACTOR = A_LONGWORD;  
288      1439 2      IF .PAT$GL_CONTEXT[ALIGN_QUAD]  
289      1440 2      THEN  
290          ALIGN_FACTOR = A_QUADWORD;  
291      1442 2      IF .PAT$GL_CONTEXT[ALIGN_PAGE]  
292      1443 2      THEN  
293          ALIGN_FACTOR = A_PAGE;  
294  
295      1446 2      !++  
296      1447 2      ! Now round up image header patch area address and alter patch area  
297      1448 2      ! size to reflect any lost bytes.  
298      1449 2      !--  
299      1450 2      PATCH_AREA_ADR = ((.PAT$GL_PATAREA[DSC$A_POINTER] + (.ALIGN_FACTOR-1))/ALIGN_FACTOR) * ALIGN_FACTOR;  
300      1451 3      IF (.PATCH_AREA_ADR NEQA .PAT$GL_PATAREA[DSC$A_POINTER])           ! If rounding actually occurred  
301      1452 3      OR (.PAT$GL_PATAREA[DSC$W_LENGTH] EQL 0)                      ! or no patch space exists  
302      1453 2      THEN  
303          BEGIN  
304              PATCH_AREA_SIZ = .PAT$GL_PATAREA[DSC$W_LENGTH] +  
305                  .PAT$GL_PATAREA[DSC$A_POINTER] - .PATCH_AREA_ADR;  
306              IF (.PATCH_AREA_SIZ LEQ 0)                                ! Check no patch area left  
307              THEN  
308                  BEGIN  
309                      IF (.PAT$GL_PATAREA[DSC$A_POINTER] EQLA .PAT$GL_IHPPTR[IHPSL_RW_PATADR])  
310                      THEN  
311                          PATSEXP_AREA (ONE_BLOCK)                         ! Get another block  
312                      ELSE  
313                          SIGNAL(PATS_NOPATAREA, 2, .PAT$GL_PATAREA[DSC$A_POINTER],  
314                                          .PAT$GL_PATAREA[DSC$W_LENGTH]);  
315                          PATCH_AREA_ADR = ((.PAT$GL_PATAREA[DSC$A_POINTER] +  
316                                          (.ALIGN_FACTOR-1))7.ALIGN_FACTOR) * ALIGN_FACTOR;  
317                          PATCH_AREA_SIZ = .PAT$GL_PATAREA[DSC$W_LENGTH] +  
318                                          .PAT$GL_PATAREA[DSC$A_POINTER] - .PATCH_AREA_ADR;
```

```

319 1470 3
320 1471 3
321 1472 3
322 1473 3
323 1474 2
324 1475 2
325 1476 2
326 1477 2
327 1478 2
328 1479 2
329 1480 2
330 1481 2
331 1482 2
332 1483 2
333 1484 2
334 1485 2
335 1486 2
336 1487 2
337 1488 2
338 1489 2
339 1490 2
340 1491 2
341 1492 2
342 1493 2
343 1494 2
344 1495 2
345 1496 2
346 1497 2
347 1498 2
348 1499 2
349 1500 2
350 1501 1

END;
PAT$GL_PATAREA[DSCSA_POINTER] = .PATCH_AREA_ADR;
PAT$GL_PATAREA[DSCSW_LENGTH] = .PATCH_AREA_SIZE;
END;

+++
Output current patch area after alignment.

--_
$FAO_TT_OUT('new patch area size: !XL', .PAT$GL_PATAREA[DSCSW_LENGTH]);
$FAO_TT_OUT('new patch area address: !XL', .PAT$GL_PATAREA[DSCSA_POINTER]);

+++
Now enter the symbol into the user-defined symbol table with a value equal
to the aligned patch area address.

--_
SYM_ENTRY_PTR = PAT$FIND_SYM(.LIST_ELEM_EXP1(.PAT$GL_HEAD_LST)); ! Check for previously defined symbol
IF .SYM_ENTRY_PTR NEQA 0 ! Yes, was previously defined
THEN ! Output informational message
BEGIN
SIGNAL(PATS REDEFSYM, 4, .SYM_CHCOUNT(.SYM_ENTRY_PTR), SYM_NAME(.SYM_ENTRY_PTR),
       .SYM_VALUE(.SYM_ENTRY_PTR), .PATCH_AREA_ADR);
SYM_VALUE(.SYM_ENTRY_PTR) = .PATCH_AREA_ADR; ! Set new value
END

ELSE
TEMP SYMTB = .PAT$GL_SYMTBPTR;
PAT$GL_SYMTBPTR = .PAT$GL_SYMHEAD;
PATSDEFINE_SYM(.LIST_ELEM_EXP1(.PAT$GL_HEAD_LST), .PATCH_AREA_ADR, TRUE); ! Enter into list
PAT$GL_SYMTBPTR = .TEMP_SYMTB;

RETURN;
END;

```

! End of PAT\$ALIGN_CMD

```

.TITLE PATSPA
.IDENT \V04-000\

.PSECT _PAT$PLIT,NOWRT,NOEXE,0

20 61 65 72 61 20 68 63 74 61 70 20 64 6C 6F 00000 P.AAA: .BYTE 28
4C 58 21 20 20 20 20 20 20 3A 65 7A 69 73 00001 .ASCII \old patch area size: !XL\

20 61 65 72 61 20 68 63 74 61 70 20 64 6C 6F 0001D P.AAB: .BYTE 28
4C 58 21 20 20 3A 73 73 65 72 64 66 61 0001E .ASCII \old patch area address: !XL\

20 61 65 72 61 20 68 63 74 61 70 20 77 65 6E 0003A P.AAC: .BYTE 28
4C 58 21 20 20 20 20 3A 65 7A 69 73 0003B .ASCII \new patch area size: !XL\

20 61 65 72 61 20 68 63 74 61 70 20 77 65 6E 00057 P.AAD: .BYTE 28
4C 58 21 20 20 3A 73 73 65 72 64 66 61 00058 .ASCII \new patch area address: !XL\

.PSECT _PAT$OWN,NOEXE,2

41 41 50 03 00000 PAT_AREA_NAME:
          .ASCII <3>\PAA\

00000003 00004 PA_NAME_DSC:
```

```

00000000' 00008 .LONG 3
:ADDRESS PAT_AREA_NAME+1

ISEC_SIZE== 20
TXTSC_SIZE== 4
PALSC_SIZE== 16
ASDSC_SIZE== 9
FWRSC_SIZE== 24
:EXTRN PATSFAO_OUT, PATSGL_PAL_LHD
:EXTRN PATSGL_ERRCODE, PATSGL_CONTEXT
:EXTRN PATSGL_FLAGS, PATSGL_IMGHDR
:EXTRN PATSGL_PATAREA, PATSGL_IHPPTR
:EXTRN PATSGL_ISELHD, PATSGL_ISETAIL
:EXTRN PATSGL_NEWVPNMX
:EXTRN PATSGL_NEWBPNMX
:EXTRN PATSGL_IMGBLKS, PATSGL_ISVADDR
:EXTRN PATSGL_HEAD_LST
:EXTRN PATSGL_SYMTBPTR
:EXTRN PATSGL_SYMHEAD, PATSALLOBLK
:EXTRN PATSREMAP, PATSDEFINE_SYM
:EXTRN PATSFIND_SYM, PAT$FREEZ
:EXTRN PATSMAP_ADDR, PATS CLOSEIN
:EXTRN PATS CLOSEOUT, PATS OPENIN
:EXTRN PATS OPENOUT, PATS READERR
:EXTRN PATS SYSERROR, PATS_WRITEERR
:WEAK ACCESS_CHECK

.PSECT _PATSCODE,NOWRT,2

.ENTRY PATSALIGN_CMD, Save R2,R3,R4,R5,R6,R7,R8,- ; 1356
      R9,R10
      MOVAB PATSGL_SYMTBPTR, R10
      MOVAB PATSFAO_OUT, R9
      P.AAA, R8
      MOVAB PATSGL_CONTEXT, R7
      MOVAB PATSGL_PATAREA, R6
      SPATSGE_PATAREA, -(SP) ; 1423
      PUSHL R8
      #2, PATSFAO_OUT
      CALLS PATSGL_PATAREA, R0 ; 1424
      MOVL 4(R0)
      PUSHL P.AAB
      PUSHAB #2, PATSFAO_OUT
      CALLS BBC #6, PATSGL_CONTEXT, 1$ ; 1430
      MOVL #1, ALIGN_FACTOR
      #4, PATSGL_CONTEXT, 2$ ; 1432
      MOVL #2, ALIGN_FACTOR
      #2, PATSGL_CONTEXT, 3$ ; 1433
      MOVL #4, ALIGN_FACTOR
      #2, PATSGL_CONTEXT, 4$ ; 1435
      MOVL #3, ALIGN_FACTOR
      #8, ALIGN_FACTOR ; 1436
      #4, ALIGN_FACTOR
      #5, ALIGN_FACTOR ; 1438
      #5, ALIGN_FACTOR ; 1439
      #5, ALIGN_FACTOR ; 1441
      #5, ALIGN_FACTOR ; 1442
      #5, ALIGN_FACTOR ; 1444
      #5, ALIGN_FACTOR ; 1445
      MOVL PATSGL_PATAREA, R2
      MOVL 4(R2), R0
      -1(ALIGN_FACTOR)[R0], R1
      DIVL2 ALIGN_FACTOR, R1

07FC 00000
SA 00000000G EF 9E 00002 MOVAB
59 00000000G EF 9E 00009 MOVAB
58 00000000' EF 9E 00010 MOVAB
57 00000000G EF 9E 00017 MOVAB
56 00000000G EF 9E 0001E MOVAB
7E 00 B6 3C 00025 MOVZWL
      58 DD 00029 PUSHL
      02 FB 0002B CALLS
      66 D0 0002E MOVL
      A0 DD 00031 PUSHL
      1D A8 9F 00034 PUSHAB
      69 02 FB 00037 CALLS
      67 06 E1 0003A BBC
      53 01 D0 0003E MOVL
      04 E1 00041 1$: #6, PATSGL_CONTEXT, 1$
      53 02 D0 00045 BBC #4, PATSGL_CONTEXT, 2$
      03 67 02 E1 00048 2$: BBC #2, PATSGL_CONTEXT, 3$
      53 04 D0 0004C MOVL #4, ALIGN_FACTOR
      03 67 03 E1 0004F 3$: BBC #3, PATSGL_CONTEXT, 4$
      53 08 D0 00053 MOVL #8, ALIGN_FACTOR
      05 67 05 E1 00056 4$: BBC #5, ALIGN_FACTOR
      53 0200 8F 3C 0005A MOVZWL #5, ALIGN_FACTOR
      52 66 D0 0005F 5$: MOVL PATSGL_PATAREA, R2
      50 04 A2 D0 00062 MOVL 4(R2), R0
      FF A340 9E 00066 MOVAB -1(ALIGN_FACTOR)[R0], R1
      51 53 C6 0006B DIVL2 ALIGN_FACTOR, R1

```

I 14
16-Sep-1984 00:57:14
14-Sep-1984 12:52:47VAX-11 Bliss-32 V4.0-742
DISK\$VMSMASTER:[PATCH.SRC]PATSPA.B32;1Page 11
(3)PA
VO

55	51	53	C5 0006E	MULL3	ALIGN_FACTOR, R1, PATCH_AREA_ADR	
	50	55	D1 00072	CMPL	PATCH_AREA_ADR, R0	1451
		04	12 00075	BNEQ	6\$	
		62	B5 00077	TSTW	(R2)	1452
	51	52	5D 12 00079	BNEQ	10\$	
	54	52	62 3C 0007B	MOVZWL	(R2), R2	1455
	51	51	50 C1 0007E	ADDL3	R0, R2, R1	
		55	C3 00082	SUBL3	PATCH_AREA_ADR, R1, PATCH_AREA_SIZ	1456
		46	14 00086	BGTR	9\$	1457
14	A1	51 00000000G	EF D0 00088	MOVL	PAT\$GL_IHPPTR, R1	
		50	D1 0008F	(CMPL	RO, 20(R1))	1460
		0B	12 00093	BNEQ	7\$	
	00000000V	EF	01 DD 00095	PUSHL	#1	1462
			01 FB 00097	CALLS	#1, PAT\$EXP_AREA	
			11 11 0009E	BRB	8\$	
			05 BB 000A0	PUSHR	#^M<R0,R2>	1464
	00000000G	00	006D811A	02 DD 000A2	PUSHL	#2
		50	8F DD 000A4	PUSHL	#7176474	
		53	04 FB 000AA	CALLS	#4, LIB\$SIGNAL	
51	53	04	66 D0 000B1	MOVL	PAT\$GL_PATAREA, RO	1466
			50 C1 000B4	ADDL3	4(R0), ALIGN_FACTOR, R1	1467
		51	51 D7 000B9	DECL	R1	1466
55	51	53	C6 000BB	DIVL2	ALIGN_FACTOR, R1	1467
	51	51	60 3C 000C2	MULL3	ALIGN_FACTOR, R1, PATCH_AREA_ADR	
50	51	04	A0 C1 000C5	MOVZWL	(R0), R1	1469
54	50	55	C3 000CA	ADDL3	4(R0), R1, RO	
		50	66 D0 000CE	SUBL3	PATCH AREA ADR, RO, PATCH_AREA_SIZ	
	04	A0	55 D0 000D1	MOVL	PAT\$GE_PATAREA, RO	1471
	60	54	B0 000D5	MOVL	PATCH AREA ADR, 4(R0)	
	7E	00	B6 3C 000D8	MOVW	PATCH AREA_SIZ, (RO)	1472
		3A	A8 9F 000DC	MOVZWL	2PAT\$GL_PATAREA, -(SP)	1478
		69	02 FB 000DF	PUSHAB	P.AAC	
	50	66	D0 000E2	CALLS	#2, PAT\$FAO OUT	
		04	A0 DD 000E5	MOVL	PAT\$GL_PATAREA, RO	1479
		57	A8 9F 000E8	PUSHL	4(R0)	
	69	02	FB 000EB	CALLS	P.AAD	
	53	6A	D0 000EE	MOVL	#2, PAT\$FAO OUT	
	6A 00000000G	EF	D0 000F1	MOVL	PAT\$GL_SYMTB PTR, TEMP SYMTB	1494
		01	DD 000F8	PUSHL	PAT\$GL_SYMHEAD, PAT\$GE_SYMTB PTR	1495
		55	DD 000FA	CALLS	#1	1496
	50 00000000G	EF	D0 000FC	MOVL	PATCH AREA ADR	
		04	A0 DD 00103	PUSHL	PAT\$GE_HEAD_LST, RO	
00000000G	EF	03	FB 00106	CALLS	#3, PAT\$DEFINE_SYM	
	6A	53	D0 0010D	MOVL	TEMP_SYMTB, PAT\$GL_SYMTB PTR	1497
		04	00110	RET		1501

: Routine Size: 273 bytes, Routine Base: _PAT\$CODE + 0000

352 1502 1 GLOBAL ROUTINE PATSBUILD_ISE (ISE_PTR,VPN,VBN,PAGE_CNT,ADR) : NOVALUE = ! Builds an ISD and enters it into I
353 1503 1
354 1504 1 ++
355 1505 1 FUNCTIONAL DESCRIPTION:
356 1506 1
357 1507 1 This routine builds a new image section descriptor. It is a normal
358 1508 1 type image section with read-write, copy-on-reference attributes.
359 1509 1 The virtual page number, virtual block number, and the page count
360 1510 1 are input parameters. The address of the image section table entry,
361 1511 1 built around the image section descriptor, is returned. The image
362 1512 1 section entry is linked into the table.
363 1513 1
364 1514 1 FORMAL PARAMETERS:
365 1515 1
366 1516 1 ISE_PTR - Pointer to image section entry built
367 1517 1 VPN - Virtual page number of image section
368 1518 1 VBN - Virtual block number of image section
369 1519 1 PAGE_CNT - Number of pages in image section
370 1520 1 ADR = [OPTIONAL] Address which is to be modified by the patch.
371 1521 1
372 1522 1 IMPLICIT INPUTS:
373 1523 1
374 1524 1 The image section table is set up.
375 1525 1
376 1526 1 IMPLICIT OUTPUTS:
377 1527 1
378 1528 1 A new image section descriptor is built.
379 1529 1
380 1530 1 ROUTINE VALUE:
381 1531 1
382 1532 1 none
383 1533 1
384 1534 1 COMPLETION CODES:
385 1535 1
386 1536 1 none
387 1537 1
388 1538 1 SIDE EFFECTS:
389 1539 1
390 1540 1 If the ADR parameter is included in the call, we will propagate the
391 1541 1 the image section attributes (of the image section containing the
392 1542 1 address specified by ADR) to the newly created default patch area.
393 1543 1
394 1544 1 --
395 1545 1
396 1546 2 BEGIN
397 1547 2
398 1548 2 BUILTIN
399 1549 2 NULLPARAMETER;
400 1550 2
401 1551 2 LOCAL
402 1552 2 PFC : BYTE,
403 1553 2 TYPE : BYTE,
404 1554 2 FLAGS,
405 1555 2 IDENT,
406 1556 2 PREV_ISE_PTR : REF BLOCK[,BYTE],
407 1557 2 TEMP : REF BLOCK[,BYTE],
408 1558 2 LOCAL_ISE_PTR : REF BLOCK[,BYTE],

: Page Fault Cluster size
: Type of image section
: Image section Flags
: Image section Ident
: Pointer to previous Image Section table entry
: Holds the FLINK from previous ISE
: Image section table entry pointer

```

409 1559 2 ISD_PTR : REF BLOCK[,BYTE];
410 1560 2 !++
411 1561 2 ! Allocate space for new image section table entry.
412 1562 2 ! ***** UNTIL SYSTEM IS UPDATED TO CONTAIN AN IDENT PERFORM TEST ON WHAT
413 1563 2 ! ***** SIZE TO USE.
414 1564 2 !-
415 1565 2 IF PATSK_LENPRIV GTR ISDSK_LENPRIV
416 1566 2 THEN PATSALLOBLK(ISESC_SIZE+PATSK_LENPRIV, .ISE_PTR)
417 1567 2 ELSE PATSALLOBLK(ISESC_SIZE+ISDSK_LENPRIV, .ISE_PTR);
418 1568 2
419 1569 2
420 1570 2
421 1571 2
422 1572 2 !++
423 1573 2 ! Now link the new entry into the table.
424 1574 2 ! This is accomplished by traversing the Image Section Table Entries, looking for any
425 1575 2 ! Non-Based Global or Stack ISDs which follow the last "Normal" ISD. When this location
426 1576 2 ! is found, the links in the affected ISEs are modified to include the new PATCH ISE.
427 1577 2 !-
428 1578 2 LOCAL_ISE_PTR = .PAT$GL_ISELHD;
429 1579 2 PREV_ISE_PTR = .LOCAL_ISE_PTR;
430 1580 2 ISD_PTR = CHSPTR (.LOCAL_ISE_PTR, ISESC_SIZE); ! Get the list head.
431 1581 2
432 1582 2 UNTIL ( (.LOCAL_ISE_PTR EQ 0) OR ! Set PREV = Current for first pass.
433 1583 2 (.ISD_PTR[ISDSB_TYPE] EQ ISDSK_USRSTACK) OR ! Point to the first ISD in the list.
434 1584 2 (.ISD_PTR[ISDSV_GBL] AND NOT .ISD_PTR[ISDSV_BASED]) ) DO
435 1585 2 BEGIN
436 1586 2 IF NOT NULLPARAMETER (5) ! Was an address included in the call?
437 1587 2 THEN ! If so, then check to see if it maps
438 1588 2 IF .ADR GEQ .ISD_PTR[ISDSV_VPN] ^9 ! into this ISD.
439 1589 2 AND .ADR LEQ ((.ISD_PTR[ISDSV_VPN] +
440 1590 2 .ISD_PTR[ISDSW_PAGCNT]) ^9) - 1
441 1591 2 THEN ! It does map into this ISD, so save the
442 1592 2 BEGIN ! attributes for the new default patch area
443 1593 2 PFC = .ISD_PTR[ISDSB_PFC]; ! Page Fault Cluster size.
444 1594 2 FLAGS = .ISD_PTR[ISDSL_FLAGS]; ! Image section Flags.
445 1595 2 TYPE = .ISD_PTR[ISDSB_TYPE]; ! Image section Type.
446 1596 2 IDENT = .ISD_PTR[ISDSL_IDENT]; ! Image section Ident.
447 1597 2 END;
448 1598 2 PREV_ISE_PTR = .LOCAL_ISE_PTR; ! Save the address of the just checked ISE.
449 1599 2 LOCAL_ISE_PTR = .LOCAL_ISE_PTR[ISESL_NXTISE]; ! Advance the pointer to the next ISE.
450 1600 2 ISD_PTR = CHSPTR (.LOCAL_ISE_PTR, ISESC_SIZE); ! Point to the next ISD also.
451 1601 2 END;
452 1602 2
453 1603 2 !++
454 1604 2 ! At this point we should be positioned to the location for inserting the new PATCH ISE/ISD pair.
455 1605 2 !-
456 1606 2 LOCAL_ISE_PTR = CHSPTR (..ISE_PTR, 0); ! Pick up the address of the new ISE.
457 1607 2 TEMP = .PREV_ISE_PTR[ISESL_NXTISE]; ! Save the FLINK to next ISE.
458 1608 2 PREV_ISE_PTR[ISESL_NXTISE] = .LOCAL_ISE_PTR; ! Set FLINK to the new ISE.
459 1609 2 LOCAL_ISE_PTR[ISESL_NXTISE] = .TEMP; ! Remember to point to the next ISE.
460 1610 2
461 1611 2 !++
462 1612 2 ! Initialize the image section table information.
463 1613 2 !-
464 1614 2 LOCAL_ISE_PTR[ISESL_MAPVST] = 0; ! Local ISE pointer to start of map.
465 1615 2 LOCAL_ISE_PTR[ISESL_MAPVEND] = 0; ! Local ISE pointer to end of map.

```

```

466 1616 2 LOCAL_ISE_PTR[ISESL_IMGVST] = 0;
467 1617 2 LOCAL_ISE_PTR[ISESL_IMGVEND] = 0;
468 1618
469 1619 !++
470 1620 ! Now build the image section descriptor.
471 1621 !--
472 1622 ISD_PTR = CHSPTR(.LOCAL_ISE_PTR, ISE$C_SIZE); ! Point to ISD
473 1623 ! ***** THIS SHOULD CHANGE WHEN IDENT-FIELD IS DEFINED FOR PROCESS PRIVATE IMAGE SECTIONS.
474 1624 ! ISD_PTR[ISDSW_SIZE] = (IF (PATSK_LENPRIV GTR ISDSK_LENPRIV) THEN PATSK_LENPRIV ELSE ISDSK_LENPRIV);
475 1625 ! *****
476 1626 ISD_PTR[ISDSW_SIZE] = ISDSK_LENPRIV;
477 1627 ISD_PTR[ISDSW_PAGCNT] = .PAGE_CNT;
478 1628 ISD_PTR[ISDSL_VPNPFC] = .VPN;
479 1629 ISD_PTR[ISDSB_PFC] = 0;
480 1630 ISD_PTR[ISDSL_FLAGS] = 0;
481 1631 ISD_PTR[ISDSV_CRF] = TRUE;
482 1632 ISD_PTR[ISDSV_WRT] = TRUE;
483 1633 ISD_PTR[ISDSV_MATCHCTL] = ISDSK_MATNEV;
484 1634 ISD_PTR[ISDSB_TYPE] = ISDSK_NORMAL;
485 1635 ISD_PTR[ISDSL_VBN] = .VBN;
486 1636 ISD_PTR[ISDSL_IDENT] = 0;
487 1637
488 1638 IF NOT NULLPARAMETER (5) ! Should we propagate the "patched"
489 1639 THEN ! image section attributes?
490 1640 BEGIN
491 1641 ISD_PTR[ISDSB_PFC] = .PFC;
492 1642 ISD_PTR[ISDSL_FLAGS] = .FLAGS;
493 1643 ISD_PTR[ISDSB_TYPE] = .TYPE;
494 1644 ISD_PTR[ISDSL_IDENT] = .IDENT;
495 1645 END;
496 1646 2 RETURN;
497 1647 1 END;
498 1648

```

! End of PAT\$BUILD_ISE

				01FC 00000	.ENTRY	PAT\$BUILD_ISE, Save R2,R3,R4,R5,R6,R7,R8	1502
			04	AC DD 00002	PUSHL	ISE_PTR	1568
				28 DD 00005	PUSHL	#40	
	00000000G	EF		02 FB 00007	CALLS	#2, PAT\$ALLOBLK	
		51	00000000G	EF DD 0000E	MOVL	PAT\$GL_ISELHD, LOCAL_ISE_PTR	1578
		53		51 D0 00015	MOVL	LOCAL_ISE_PTR, PREV_ISE_PTR	1579
		50	14	A1 9E 00018	1\$: MOVAB	20(R1), ISD_PTR	1580
				51 D5 0001C	TSTL	LOCAL_ISE_PTR	1582
				5B 13 0001E	BEQL	4\$	
		FD	8F	08 A0 91 00020	CMPB	11(ISD_PTR), #253	1583
				54 13 00025	BEQL	4\$	
		4B	09	05 08 A0 E9 00027	BLBC	8(ISD_PTR), 2\$	1584
				01 E1 0002B	BBC	#1, 9TISD_PTR), 4\$	
			05	6C 91 00030	CMPB	(AP), #5	1586
				3E 1F 00033	BLSSU	3\$	
				14 AC D5 00035	TSTL	20(AP)	
				39 13 00038	BEQL	3\$	
52	04	A0	15	00 EF 0003A	EXTZV	#0, #21, 4(ISD_PTR), R2	1588
		52		09 78 00040	ASHL	#9, R2, R2	

M 14
16-Sep-1984 00:57:14
14-Sep-1984 12:52:47VAX-11 Bliss-32 V4.0-742
DISK\$VMSMASTER:[PATCH.SRC]PATSPA.B32:1Page 15
(4)

			52	14	AC D1 00044	CMPL	ADR, R2		
52	04 A0		15	29	19 00048	BLSS	3\$		
			58	00	EF 0004A	EXTZV	#0, #21, 4(ISD_PTR), R2		
			58	02	A0 3C 00050	MOVZWL	2(ISD_PTR), R8-		
		52	52	58	C0 00054	ADDL2	R8, R2		
		52	52	09	78 00057	ASHL	#9, R2, R2		
		52	52	52	D7 0005B	DECL	R2		
		52	52	10	14 00061	CMPL	ADR, R2		
		57	57	07	A0 90 00063	BGTR	3\$		
		55	55	08	A0 D0 00067	MOVB	7(ISD_PTR), PFC		
		56	56	0B	A0 90 0006B	MOVL	8(ISD_PTR), FLAGS		
		54	54	10	A0 D0 0006F	MOVB	11(ISD_PTR), TYPE		
		53	53	51	D0 00073	MOVL	16(ISD_PTR), IDENT		
		51	51	61	D0 00076	MOVL	LOCAL_ISE_PTR, PREV_ISE_PTR (LOCAL_ISE_PTR), LOCAL_ISE_PTR		
				9D	11 00079	BRB	1\$		
		51	51	04	BC D0 0007B	MOVL	@ISE_PTR, LOCAL_ISE_PTR		
			52	63	D0 0007F	MOVL	(PREV_ISE_PTR), TEMP		
		63	63	51	D0 00082	MOVL	LOCAL_ISE_PTR, (PREV_ISE_PTR)		
		61	61	52	D0 00085	MOVL	TEMP, (LOCAL_ISE_PTR)		
			0C	A1 7C 00088	CLRQ	12(LOCAL_ISE_PTR)			
		50	50	04	A1 7C 0008B	CLRQ	4(LOCAL_ISE_PTR)		
		60	60	14	A1 9E 0008E	MOVAB	20(R1), ISD_PTR		
		02	A0	10	A0 B0 00092	MOVW	#16, (ISD_PTR)		
		04	A0	08	AC D0 00095	MOVW	PAGE_CNT, 2(ISD_PTR)		
				07	A0 94 0009A	MOVL	VPN, 4(ISD_PTR)		
		52	52	08	A0 9E 000A2	CLRB	7(ISD_PTR)		
				62	D4 000A6	MOVAB	8(ISD_PTR), R2		
		62	62	0A	88 000A8	CLRL	(R2)		
		03	03	03	F0 000AB	BISB2	#10, (R2)		
		0C	A0	08	A0 94 000B0	INSV	#3, #4, #3, (R2)		
			0C	AC	D0 000B3	CLRB	11(ISD_PTR)		
			10	A0	D4 000B8	MOVL	VBN, 12(ISD_PTR)		
		05	05	6C	91 000BB	CLRL	16(ISD_PTR)		
				14	1F 000BE	CMPB	(AP), #5		
				14	AC D5 000C0	BLSSU	5\$		
				0F	13 000C3	TSTL	20(AP)		
		07	A0	57	90 000C5	BEQL	5\$		
		62	62	55	D0 000C9	MOVB	PFC, 7(ISD_PTR)		
		08	A0	56	90 000CC	MOVL	FLAGS, (R2)		
		10	A0	54	D0 000D0	MOVB	TYPE, 11(ISD_PTR)		
				04	000D4	MOVL	IDENT, 16(ISD_PTR)		
				58:		RET			

: Routine Size: 213 bytes. Routine Base: _PATSCODE + 0111

500 1649 1 GLOBAL ROUTINE PATSEXP_AREA (NUM_BLKS, ADR) : NOVALUE = ! Expands patch area
501 1650 1
502 1651 1 ++
503 1652 1 FUNCTIONAL DESCRIPTION:
504 1653 1
505 1654 1 This routine expands the read-write patch area defined in the image
506 1655 1 header. If there is no patch area, then an image section descriptor
507 1656 1 is created for it. If the image section which is being created is a
508 1657 1 due to either an INSERT or REPLACE command then the attributes of the
509 1658 1 image section are propagated to the new image section. In either case,
510 1659 1 the image header is updated to describe the expanded patch area.
511 1660 1
512 1661 1 If the patch area is mapped to the highest address used during this
513 1662 1 patch session, then the patch area can be expanded contiguously.
514 1663 1 In this case, the image section descriptor is updated to hold a new
515 1664 1 page count and the patch area size in the image header is increased.
516 1665 1 If the patch area is not the highest address used, then the patch area
517 1666 1 must be relocated to another area, which will be contiguous. This
518 1667 1 involves expanding the program region, copying in the old patch area,
519 1668 1 and then changing the image section table entry to point to a new
520 1669 1 mapped address. The image header and image section descriptor counts
521 1670 1 are incremented as above.
522 1671 1
523 1672 1 NOTE: The patch area must be mapped contiguously in order for
524 1673 1 the mapping of addresses to work. It could also be accomplished
525 1674 1 if two image section table entries were created. However, this
526 1675 1 would require an extra, unnecessary image section descriptor.
527 1676 1
528 1677 1 Some of the PATCH commands which deposit symbolic instructions do an
529 1678 1 PAT\$EXPAREA just to force the address to be non-zero so that
530 1679 1 the symbolic instruction encoder can correctly encode operands.
531 1680 1
532 1681 1 FORMAL PARAMETERS:
533 1682 1
534 1683 1 NUM_BLKS - Number of blocks to be allocated for the patch area
535 1684 1 ADR-[OPT] The address which we will use to propagate the image section
536 1685 1 attributes.
537 1686 1
538 1687 1 IMPLICIT INPUTS:
539 1688 1
540 1689 1 The image header and image section entry table must be set up.
541 1690 1
542 1691 1 IMPLICIT OUTPUTS:
543 1692 1
544 1693 1 none
545 1694 1
546 1695 1 ROUTINE VALUE:
547 1696 1
548 1697 1 none
549 1698 1
550 1699 1 COMPLETION CODES:
551 1700 1
552 1701 1 none
553 1702 1
554 1703 1 SIDE EFFECTS:
555 1704 1
556 1705 1 A new patch area is set up. The image header is updated to

557 1706 1 ! describe the new patch area.
558 1707 1 !
559 1708 1 !
560 1709 1 !
561 1710 1 !
562 1711 1 !
563 1712 1 !
564 1713 1 !--
565 1714 1 !
566 1715 2 BEGIN
567 1716 2 !
568 1717 2 BUILTIN
569 1718 2 !
570 1719 2 NULLPARAMETER:
571 1720 2 !
572 1721 2 LITERAL
573 1722 2 !
574 1723 2 START_OFFSET = 0,
575 1724 2 ! Offset to starting address
576 1725 2 END_OFFSET = 1;
577 1726 2 ! Offset to ending address
578 1727 2 !
579 1728 2 MAPPED_ADDR:
580 1729 2 !
581 1730 2 IF .PATSGL_FLAGS [PATSS_ABSOLUTE]
582 1731 2 THEN SIGNAL (PAT\$_DATTOOLNG);
583 1732 2 !
584 1733 2 !++
585 1734 2 ! If this is a non-PIC shareable image we do not expand the patch area to protect images
586 1735 2 ! previously linked against having inconsistent Global Section Descriptors. Else, if it
587 1736 2 ! is a PIC shareable image, we may without reservation, expand the patch area.
588 1737 2 !--
589 1738 3 IF ((.PATSGL_IMGHDR[IHDSB_IMGTYP] EQLU IHDSK_LIM) AND (NOT .PATSGL_IMGHDR[IHDSV_PICIMG]))
590 1739 2 THEN
591 1740 2 SIGNAL(PAT\$_EXPSHRPAT+MSG\$K_SEVERE);
592 1741 2 !
593 1742 2 !++
594 1743 2 ! If there is no patch area defined yet, then build an image section table
595 1744 2 ! entry and an image section descriptor for it.
596 1745 2 !--
597 1746 3 IF (.PATSGL_IHPPTR[IHPSL_RW_PATADR] EQLA 0)
598 1747 2 THEN
599 1748 2 BEGIN
600 1749 2 !++
601 1750 2 ! Build an Image Section table entry as no Patch Area was defined.
602 1751 2 !--
603 1752 2 !
604 1753 2 IF NULLPARAMETER (2)
605 1754 2 THEN PAT\$BUILD_ISE(ISE_PTR, .PATSGL_NEWVPNMX+1, .PATSGL_NEWVBNMX+1, .NUM_BLKS)
606 1755 2 ELSE PAT\$BUILD_ISE(ISE_PTR, .PATSGL_NEWVPNMX+1, .PATSGL_NEWVBNMX+1, .NUM_BLKS, .ADR);
607 1756 2 ISD_PTR = CHSPTR(.ISE_PTR, ISE\$C_SIZE);
608 1757 2 END
609 1758 2 ELSE
610 1759 2 BEGIN
611 1760 2 !++
612 1761 2 ! Find the image section table entry which describes the patch area.
613 1762 2 !--
614 1763 2 PAT\$MAP_ADDR(.PATSGL_IHPPTR[IHPSL_RW_PATADR], MAPPED_ADDR, ISE_PTR);

```
614 1763 3 ISD_PTR = CHSPTR(.ISE_PTR, ISE$C_SIZE);
615 1764 3 ISD_PTR[ISDSW_PAGCNT] = .SD_PTR[ISDSW_PAGCNT] + .NUM_BLKS; ! Expand size of image section
616 1765 2 END;
617 1766 2 ++
618 1767 2 : Update the VPN and VBN for the last ones used in the new image for
619 1768 2 : the image section.
620 1769 2 --
621 1770 2 PATSGL_NEWVPNMX = .PATSGL_NEWVPNMX + .NUM_BLKS;
622 1771 2 PATSGL_NEWVBNMX = .PATSGL_NEWVBNMX + .NUM_BLKS;
623 1772 2 ++
624 1773 2 : Now create the patch area, i.e., map it into the image. This is done
625 1774 2 : with an expand region instead of a create and map as the area is not defined
626 1775 2 : in the old image.
627 1776 2 --
628 1777 2 P 1779 2 PATSGL_ERRCODE = $EXPREG(PAGCNT = .ISD_PTR[ISDSW_PAGCNT]
629 1780 2 , RETADR = PATSGL_ISVADDR);
630 1781 2 IF NOT .PATSGL_ERRCODE
631 1782 2 THEN
632 1783 2 SIGNAL(PATS_SYSERROR, 0, .PATSGL_ERRCODE);
633 1784 2 ++
634 1785 2 : If the patch area was expanded, and not created, then copy in the old
635 1786 2 : patch area part.
636 1787 2 --
637 1788 2 1789 3 IF (.ISD_PTR[ISDSW_PAGCNT] NEQ .NUM_BLKS)
638 1790 2 THEN
639 1791 2 CH$MOVE((.ISD_PTR[ISDSW_PAGCNT] - .NUM_BLKS) * A_PAGE,
640 1792 2 .ISE_PTR[ISE$L_MAPVST], .PATSGL_ISVADDR[START_OFF]);
641 1793 2 ++
642 1794 2 : Initialize the image section table entry.
643 1795 2 --
644 1796 2 1797 2 ISE_PTR[ISE$L_MAPVST] = .PATSGL_ISVADDR[START OFF];
645 1798 2 ISE_PTR[ISE$L_MAPVEND] = .PATSGL_ISVADDR[END OFF];
646 1799 2 ISE_PTR[ISE$L_IMGVST] = .ISD_PTR[ISDSV_VPN] * 9;
647 1800 2 ISE_PTR[ISE$L_IMGVEND] = ((.ISD_PTR[ISDSV_VPN] + .ISD_PTR[ISDSW_PAGCNT]) * 9) - 1;
648 1801 2 ++
649 1802 2 : Increment the number of blocks in the new image.
650 1803 2 --
651 1804 2 1805 2 PATSGL_IMGBLKS = .PATSGL_IMGBLKS + .NUM_BLKS;
652 1806 2 ++
653 1807 2 : Update the patch area descriptor in the image header.
654 1808 2 --
655 1809 2 1810 2 PATSGL_PATAREA[DSCSW_LENGTH] = .PATSGL_PATAREA[DSCSW_LENGTH] + (.NUM_BLKS * A_PAGE);
656 1811 3 IF (.PATSGL_PATAREA[DSCSA_POINTER] EQ 0)
657 1812 2 THEN
658 1813 2 PATSGL_PATAREA[DSCSA_POINTER] = .ISE_PTR[ISE$L_IMGVST];
659 1814 2 ++
660 1815 2 : Now update the patch area list entry for the default patch area.
661 1816 2 --
662 1817 2 1818 2 PAT$ADD_PAL(.ISE_PTR[ISE$L_IMGVST], .ISE_PTR[ISE$L_IMGVEND], PALS$EXP_PAREA);
663 1819 2
```

: 671 1820 2 RETURN:
 : 672 1821 2
 : 673 1822 1 END:

! END OF PATSEXP_AREA

				.EXTRN	SYSEXPREG	
			OFFC 00000	.ENTRY	PATSEXP_AREA, Save R2,R3,R4,R5,R6,R7,R8,R9,-: 1649	
			SB 00000000G EF 9E 00002	MOVAB	R10, R11	
			5A 00000000G EF 9E 00009	MOVAB	PAT\$GL_NEUVBNMX, R11	
			59 00000000G EF 9E 00010	MOVAB	PAT\$GL_ERRCODE, R10	
			58 00000000G 00 9E 00017	MOVAB	PAT\$GL_ISVADDR, R9	
			SE 08 C2 0001E	SUBL2	LIB\$SIGNAL, R8	
			EF 06 E1 00021	BBC	#8, SP	
			006D80A2 8F DD 00029	PUSHL	#6, PAT\$GL_FLAGS, 1\$	1729
			68 01 FB 0002F	CALLS	#7176354	1730
09	00000000G		50 00000000G EF DO 00032	MOVL	#1, LIB\$SIGNAL	
			02 11 A0 91 00039	CMPB	PA\$GL_IMGHDR, R0	1738
			OE 12 0003D	BNEQ	17(R0), #2	
09	20	A0	006D82D2 8F DD 00044	BBS	2\$	
			68 01 FB 0004A	PUSHL	#3, 32(R0), 2\$	
			57 04 AC DO 0004D	CALLS	#7176914	1740
			50 00000000G EF DO 00051	MOVL	#1, LIB\$SIGNAL	
			14 A0 C5 00058	TSTL	NUM BLKS, R7	1753
			39 12 0005B	BNEQ	PAT\$GL_IHPPTR, R0	1746
51	50 00000000G	6B	02 01 C1 0005D	ADDL3	20(R0)	
			EF 01 C1 00061	ADDL3	#1, PAT\$GL_NEUVBNMX, R1	1753
			02 6C 91 00069	CMPB	#1, PAT\$GL_NEUVPNMX, R0	
			05 1F 0006C	BLSSU	(AP), #2	1752
			08 AC D5 0006E	TSTL	3\$	
			OE 12 00071	BNEQ	8(AP)	
			0083 8F BB 00073	PUSHR	4\$	
			0C AE 9F 00077	PUSHAB	#^M<R0,R1,R7>	1753
FEAC	CF		04 FB 0007A	CALLS	ISE_PTR	
			0F 11 0007F	BRB	#4, PAT\$BUILD_ISE	
			08 AC DD 00081	PUSHL	5\$	
			0083 8F BB 00084	PUSHR	ADR	1754
			10 AE 9F 00088	PUSHAB	#^M<R0,R1,R7>	
56	FE9B	CF	05 FB 0008B	CALLS	ISE_PTR	
			14 C1 00090	ADDL3	#5, PAT\$BUILD_ISE	1755
			17 11 00094	BRB	#20, ISE_PTR, ISD_PTR	1746
			5E DD 00096	PUSHL	7\$	1762
			68 AE 9F 00098	PUSHAB	SP	
			14 AO DD 0009B	PUSHL	MAPPED-ADDR	
			08 03 FB 0009E	CALLS	20(R0)	
56	00000000G	EF	6E 14 C1 000A5	ADDL3	#3, PAT\$MAP_ADDR	
			02 A6 57 AO 000A9	ADDW2	#20, ISE_PTR, ISD_PTR	1763
			00000000G EF 57 C0 000AD	ADDL2	R7, 2(ISD_PTR)	1764
			6B 57 C0 00084	ADDL2	R7, PAT\$GL_NEUVPNMX	1771
			7E 7C 000B7	CLRQ	R7, PAT\$GL_NEUVBNMX	1772
			59 DD 000B9	PUSHL	-(\$P)	1780
			00 7E 02 A6 3C 000BB	MOVZWL	R9	
			6A 04 FB 000BF	CALLS	2(ISD_PTR), -(SP)	
			50 00 000C6	MOVL	#4, SYSEXPREG	
					RO, PAT\$GL_ERRCODE	

			0D		6A E8 000C9	BLBS	PAT\$GL_ERRCODE, 8\$	1781
			68 00000000G		6A DD 000CC	PUSHL	PAT\$GL_ERRCODE	1783
57	02 A6		10		7E D4 000CE	CLRL	-(SP)	
			51 02		8F DD 000DO	PUSHL	#PAT\$ SYSERROR	
			51		03 FB 000D6	CALLS	#3, LIB\$SIGNAL	
			51		00 ED 000D9	CMPZV	#0, #16, 2(ISD_PTR), R7	1789
			51		14 13 000DF	BEQL	9\$	
			51		A6 3F 000E1	MOVZWL	2(ISD_PTR), R1	1791
			51		57 C2 000E5	SUBL2	R7, RT	
	00 B9	0C	50		09 78 000E8	ASHL	#9, R1, R1	1792
			50		6E D0 000EC	MOVL	ISÉ_PTR, R0	
		0C	A0		51 28 000EF	MOVC3	R1, @12(R0), APAT\$GL_ISVADDR	
51	04 A6		15		6E D0 000F5	MOVL	ISÉ_PTR, R0	1797
51	04 A0		51		09 7D 000F8	EXTZV	PAT\$GL_ISVADDR, 12(R0)	
51	04 A6		15		00 EF 000FC	ASHL	#0, #2T, 4(ISD_PTR), R1	1799
			56 02		09 78 00102	EXTZV	#9, R1, 4(R0)	
			56		00 EF 00107	MOVZWL	#0, #21, 4(ISD_PTR), R1	1800
			56		A6 3C 0010D	ADDL2	2(ISD_PTR), R6	
			56		51 C0 00111	ASHL	R1, R6	
			08		09 78 00114	MOVAB	#9, R6, R6	
			A0 FF		A6 9E 00118	-1(R6), 8(R0)		
			EF 00000000G		57 C0 0011D	ADDL2	R7, PAT\$GL_IMGBLKS	1805
52			51 00000000G		EF D0 00124	MOVL	PAT\$GL_PATAREA, R1	1810
			57		09 78 0012B	ASHL	#9, R7, R2	
			61		52 A0 0012F	ADDW2	R2, (R1)	
			04		A1 D5 00132	TSTL	4(R1)	1811
			04		05 12 00135	BNEQ	10\$	
	04 A1		04		A0 D0 00137	MOVL	4(R0), 4(R1)	1813
			04		01 DD 0013C	PUSHL	#1	1818
			7E EF		A0 7D 0013E	MOVL	4(R0), -(SP)	
			00000000V		03 FB 00142	CALLS	#3, PAT\$ADD_PAL	
					04 00149	RET		1822

: Routine Size: 330 bytes. Routine Base: _PAT\$CODE + 01E6

675 1823 1 GLOBAL ROUTINE PAT\$ADD_PAL (START_ADR, END_ADR, PAT_AREA_FLAG) : NOVALUE = ! EXPANDS PATCH AREAS
676 1824 1
677 1825 1 ++
678 1826 1 FUNCTIONAL DESCRIPTION:
679 1827 1
680 1828 1 THIS ROUTINE MAINTAINS THE PATCH AREA LIST (PAL). THIS INCLUDES
681 1829 1 UPDATING THE ENTRY FOR THE DEFAULT PATCH AREA WHENEVER PATCH EXPANDS
682 1830 1 IT AND CREATING ENTRIES WHENEVER THE USER ISSUES A "SET PATCH AREA"
683 1831 1 COMMAND. THE FIRST ENTRY ON THE LIST IS ALWAYS THE DEFAULT PATCH AREA.
684 1832 1
685 1833 1 THE PATCH AREA LIST IS USED TO CORRECTLY OUTPUT ADDRESSES FOR
686 1834 1 PATCH AREA TO THE OUTPUT COMMAND FILE. THESE ADDRESSES MUST BE
687 1835 1 WRITTEN TO THE FILE AS SYMBOLIC NAMES PLUS OFFSETS BECAUSE THE
688 1836 1 IMAGES IN THE FIELD MAY HAVE BEEN PATCHED BY CUSTOMERS (THUS
689 1837 1 CHANGING THE NEXT FREE PATCH AREA ADDRESS). BY OUTPUTTING PATCH
690 1838 1 AREA ADDRESSES AS SYMBOLIC NAMES, PATCH WILL PERMIT PATCHES TO
691 1839 1 USE DIFFERENT PATCH AREA ADDRESSES.
692 1840 1
693 1841 1 AN ENTRY IN THE PATCH AREA LIST HAS THE FOLLOWING FORMAT:
694 1842 1
695 1843 1
696 1844 1 FORWARD LINK : PALS_L_FLINK
697 1845 1
698 1846 1 STARTING ADDRESS : PALS_L_ST_ADR
699 1847 1
700 1848 1 ENDING ADDRESS : PALS_L_END_ADR
701 1849 1
702 1850 1 PATCH AREA NAME : PALS_L_CS_NAME
703 1851 1
704 1852 1
705 1853 1 THE PATCH AREA NAME CONSISTS OF AN ASCII STRING, WHICH IS ALWAYS A
706 1854 1 COUNT OF THREE FOLLOWED BY THE ASCII CHARACTERS "P", "A", AND A THIRD
707 1855 1 CHARACTER RANGING FROM "A" TO "Z". THIS NAME IS USED TO OUTPUT
708 1856 1 SYMBOLIC REFERENCES TO THE OUTPUT COMMAND FILE FOR ALL ADDRESSES WITHIN
709 1857 1 THE PATCH AREAS INSTEAD OF ABSOLUTE VALUES.
710 1858 1
711 1859 1 THIS ROUTINE ALSO CAUSES A SYMBOL TO BE DEFINED FOR THE STARTING ADDRESS
712 1860 1 OF THE PATCH AREA.
713 1861 1
714 1862 1 FORMAL PARAMETERS:
715 1863 1
716 1864 1 START_ADR - STARTING ADDRESS OF THE PATCH AREA
717 1865 1 END_ADR - ENDING ADDRESS OF THE PATCH AREA
718 1866 1 PAT_AREA_FLAG - INDICATOR FOR TYPE OF PAL UPDATE
719 1867 1 PALK_EXP_PAREA = 1 - EXPANDING DEFAULT PATCH AREA
720 1868 1 PALK_ADD_PAREA = 0 - ADDING NEW PATCH AREA ENTRY
721 1869 1
722 1870 1 IMPLICIT INPUTS:
723 1871 1
724 1872 1 THE FREE STORAGE ROUTINES MUST HAVE BEEN INITIALIZED.
725 1873 1
726 1874 1 IMPLICIT OUTPUTS:
727 1875 1
728 1876 1 NONE
729 1877 1
730 1878 1
731 1879 1 ROUTINE VALUE:

732 1880 1 | NONE
733 1881 1 |
734 1882 1 | COMPLETION CODES:
735 1883 1 |
736 1884 1 | NONE
737 1885 1 |
738 1886 1 | SIDE EFFECTS:
739 1887 1 |
740 1888 1 | THE PATCH AREA LIST IS UPDATED. EITHER AN ENTRY IS MODIFIED OR
741 1889 1 | A NEW LINK IS CREATED. IN THE LATTER CASE, THE NEXT PATCH AREA NAME
742 1890 1 | IS ALSO UPDATED. THE NEXT PATCH AREA NAME IS ALSO UPDATED.
743 1891 1 |
744 1892 1 |--
745 1893 1 |
746 1894 2 BEGIN
747 1895 2 |
748 1896 2 LOCAL
749 1897 2 TEMP SYMTB,
750 1898 2 NEW_PTR : REF BLOCK[BYTE],
751 1899 2 TEMP_PTR : REF BLOCK[BYTE],
752 1900 2 NAME_DESC : BLOCK[8,BYTE];
753 1901 2 | Temporary symbol table pointer
754 1902 2 | POINTER TO NEW PAL ENTRY
755 1903 2 | POINTER TO CURRENT PAL ENTRY
756 1904 2 | STRING DESCRIPTOR FOR DEFAULT PATCH AREA N
757 1905 2 |
758 1906 2 |++ FIRST, LOOP THROUGH THE PATCH AREA LIST TRYING TO FIND AN ENTRY FOR THIS
759 1907 2 | PATCH AREA, I.E., HAS THIS PATCH AREA JUST BEEN EXPANDED. IF SO, UPDATE
760 1908 2 | THE PAL ENTRY AND RETURN. IF NOT, FALL THROUGH TO CREATE A NEW PAL ENTRY.
761 1909 2 |--
762 1910 2 | TEMP_SYMTB = .PAT\$GL_SYMTBPTR;
763 1911 3 | IF (TEMP_PTR = CHSPTR(.PAT\$GL_PA_LHD, 0)) NEQ 0
764 1912 3 | THEN REPEAT
765 1913 3 | BEGIN
766 1914 3 | |++
767 1915 3 | | IF THE DEFAULT PATCH AREA WAS CREATED, THEN BOTH THE STARTING
768 1916 3 | | AND ENDING ADDRESSES MUST BE RESET. IF THE DEFAULT PATCH
769 1917 3 | | AREA WAS EXPANDED, THEN THE STARTING ADDRESS REMAINS THE
770 1918 3 | | SAME AND THE ENDING ADDRESS IS UPDATED. THIS WILL NEED
771 1919 3 | | SOME NEW INVENTION WHEN READ-ONLY PATCH AREAS ARE
772 1920 3 | | ALSO ADDED.
773 1921 3 | |--
774 1922 4 | | IF .PAT_AREA_FLAG EQA PALS\$EXP_PAREA
775 1923 4 | | THEN BEGIN
776 1924 4 | | | TEMP_PTR[PALSL-ENDADR] = .ENDADR;
777 1925 4 | | | IF .TEMP_PTR[PALSL-STARTADR] EQA 0
778 1926 5 | | | THEN BEGIN
779 1927 5 | | | | TEMP_PTR[PALSL-STARTADR] = .STARTADR;
780 1928 5 | | | | NAME_DESC[DSC\$0_LENGTH] = .PAT_AREA_NAME[0];
781 1929 5 | | | | NAME_DESC[DSC\$A_POINTER] = CHSPTR(TEMP_PTR[PALSL_CS_NAME], 1);
782 1930 5 | | | | PAT\$GL_SYMTBPTR = .PAT\$GL_SYMHEAD;
783 1931 5 | | | | PAT\$DEFINE_SYM(NAME_DESC, .STARTADR, FALSE);
784 1932 5 | | | | PAT\$GL_SYMTBPTR = .TEMP_SYMTB;
785 1933 4 | | | | END;
786 1934 4 | | | RETURN;
787 1935 3 | | | END;
788 1936 3 | | | IF (.STARTADR GEQA .TEMP_PTR[PALSL_STARTADR]) AND

```

789 1937 4 (.END_ADR EQLA .TEMP_PTR[PALSL_END_ADR])
790 1938 3 THEN
791 1939 3 RETURN:
792 1940 3 IF .TEMP_PTR[PALSL_FLINK] NEQA 0
793 1941 3 THEN .TEMP_PTR = .TEMP_PTR[PALSL_FLINK]
794 1942 3 ELSE EXITLOOP;
795 1943 3 END:
796 1944 2
797 1945 2
798 1946 2
799 1947 2 ++
800 1948 2 | THERE WAS NO CORRESPONDING PAL ENTRY. THEREFORE A NEW ENTRY MUST BE CREATED.
801 1949 2 --
802 1950 2 NEW_PTR = PAT$FREEZ((PALSC_SIZE + A_LONGWORD - 1)/A_LONGWORD); ! ALLOCATE SPACE FOR NEW ENTRY
803 1951 2 IF .TEMP_PTR EQLA 0
804 1952 2 THEN
805 1953 2 | PAT$GL_PAL_LHD = CH$PTR(.NEW_PTR, 0) ! SET THE LIST HEAD
806 1954 2 ELSE
807 1955 2 | TEMP PTR[PALSL_FLINK] = .NEW_PTR; ! LINK IN NEW ENTRY
808 1956 2 | NEW_PTR[PALSL_START_ADR] = .START_ADR; ! SET STARTING PATCH AREA ADDRESS
809 1957 2 | NEW_PTR[PALSL_END_ADR] = .END_ADR; ! SET ENDING PATCH AREA ADDRESS
810 1958 2 | CH$MOVE(A_LONGWORD, PAT_AREA_NAME, NEW_PTR[PALSL_CS_NAME]); ! SET PATCH AREA NAME
811 1959 2 | PAT$GL_SYMTB PTR = .PAT$GL_SYMTB; ! Use user-defined symbol table
812 1960 2 | PAT$DEFINE SYM(PA_NAME DSC, .NEW_PTR[PALSL_START_ADR], FALSE); ! DEFINE SYMBOL AS START OF PATCH AREA
813 1961 2 | PAT$GL_SYMTB PTR = .TEMP_SYMTB; ! Restore label symbol table
814 1962 2 | PAT_AREA_NAME[3] = .PAT_AREA_NAME[3] + 1; ! SET NEW PATCH AREA NAME
815 1963 2
816 1964 2 ++
817 1965 2 | NOW CHECK THAT THE NEXT PATCH AREA NAME IS BETWEEN "PAA" AND "PAZ". IF
818 1966 2 | IT IS NOT, THE RESET THE THIRD CHARACTER OF THE NAME TO AN "A" AND
819 1967 2 | INCREMENT THE SECOND LETTER OF THE NAME. THIS WILL ALLOW THE USER TO DEFINE
820 1968 2 | UP TO 676 PATCH AREAS.
821 1969 2 --
822 1970 3 | IF .PAT_AREA_NAME[3] GTRU (%ASCII'Z')
823 1971 2 | THEN ! CHECK FOR OVERFLOW OF PATCH AREA NAMES
824 1972 3 | BEGIN
825 1973 3 | | PAT_AREA_NAME[2] = .PAT_AREA_NAME[2] + 1; ! INCREMENT THE "A" OF "PAZ"
826 1974 3 | | PAT_AREA_NAME[3] = (%ASCII'A'); ! CHANGE THE "Z" TO AN "A"
827 1975 2 | END:
828 1976 2
829 1977 2 | RETURN;
830 1978 2
831 1979 1 | END: ! END OF PAT$ADD_PAL

```

58	00000000G	01FC 00000	ENTRY	PAT\$ADD_PAL, Save R2,R3,R4,R5,R6,R7,R8	: 1823
57	00000000G	EF 9E 00002	MOVAB	PAT\$DEFINE SYM, R8	
56	00000000G	EF 9E 00009	MOVAB	PAT\$GL_SYMTB PTR, R7	
55	00000000G	EF 9E 00010	MOVAB	PAT\$GL_PAL_LHD, R6	
54	00000000'	EF 9E 00017	MOVAB	PAT\$GL_SYMTB PTR, R5	
5E		08 C2 00025	MOVAB	PAT_AREA_NAME+3, R4	
53		65 D0 00028	SUBL2	#8,-SP	
52		66 D0 0002B	MOVL	PAT\$GL_SYMTB PTR, TEMP_SYMTB	: 1907
			MOVL	PAT\$GL_PAL_LHD, TEMP_PTR	: 1908

			47	13	0002E		BEQL	4\$		
		01	0C	D1	00030	1\$:	CMPL	PAT_AREA_FLAG, #1		1920
			2A	12	00034		BNEQ	2\$		
	08	A2	08	AC	D0	00036	MOVL	END_ADR, 8(TEMP_PTR)		1923
			04	A2	D5	0003B	TSTL	4(TEMP_PTR)		1924
	04	A2	04	AC	D0	00040	BNEQ	7\$		
			6E	FD	A4	9B	00045	MOVL	START_ADR, 4(TEMP_PTR)	1927
	04	AE	0D	A2	9E	00049	MOVZBW	PAT_AREA_NAME, NAME_DESC		1928
			65		67	D0	0004E	MOVAB	13(R2), NAME_DESC+1	1929
					7E	D4	00051	MOVL	PAT\$GL_SYMHEAD, PAT\$GL_SYMTBPTR	1930
					04	AC	DD	CLRL	-(SP)	1931
					08	AE	9F	PUSHL	START_ADR	
	68				03	FB	00059	PUSHAB	NAME DESC	
	65				53	D0	0005C	CALLS	#3, PAT\$DEFINE_SYM	
					04	0005F	MOVL	TEMP_SYMTB, PAT\$GL_SYMTBPTR		1932
							RET			1922
	04	A2	04	AC	D1	00060	2\$::	CMPL	START_ADR, 4(TEMP_PTR)	1936
					07	1F	00065	BLSSU	3\$	
	08	A2	08	AC	D1	00067	48	CMPL	END_ADR, 8(TEMP_PTR)	1937
					13	0006C	62	BNEQ	7\$	
					13	0006E	05	TSTL	(TEMP_PTR)	1940
					00070	62	D0	BEQL	4\$	
		52			00072	B9	11	MOVL	(TEMP_PTR), TEMP_PTR	1942
					00075		04	BRB	1\$	
			0000000G	EF	00077	01	FB	PUSHL	#4	1950
					4\$:	52	D5	CALLS	#1, PAT\$FREEZ	
						05	00080	TSTL	TEMP_PTR	1951
		66				12	00082	BNEQ	5\$	
						50	D0	MOVL	NEW_PTR, PAT\$GL_PAL_LHD	1953
						03	11	BRB	6\$	
	04	62	04	AC	50	00084	50	MOVL	NEW_PTR, (TEMP_PTR)	1955
	0C	A0	FD	A4	7D	0008C	6\$::	MOVQ	START_ADR, 4(NEW_PTR)	1956
					00091	65	D0	MOVL	PAT_AREA_NAME, 12(NEW_PTR)	1958
					00096	67	D0	MOVL	PAT\$GL_SYMHEAD, PAT\$GL_SYMTBPTR	1959
					00099	7E	D4	CLRL	-(SP)	1960
					0009B	04	A0	PUSHL	4(NEW_PTR)	
					0009E	01	A4	PUSHAB	PA_NAME_DSC	
	68				03	FB	000A1	CALLS	#3, PAT\$DEFINE_SYM	
	65				53	D0	000A4	MOVL	TEMP_SYMTB, PAT\$GL_SYMTBPTR	1961
					64	96	000A7	INC8	PAT_AREA_NAME+3	1962
	5A	8F			64	91	000A9	CMPB	PAT_AREA_NAME+3, #90	1970
					07	1B	000AD	BLEQU	7\$	
					FF	A4	96	INC8	PAT_AREA_NAME+2	1973
	64				41	8F	90	MOVB	#65, PAT_AREA_NAME+3	1974
					04	000B2	04	RET		1979
						000B6	7\$::			

: Routine Size: 183 bytes. Routine Base: _PAT\$CODE + 0330

PATSPA
V04-000

: 833 1980 1 END
: 834 1981 0 ELUDOM

J 15
16-Sep-1984 00:57:14
14-Sep-1984 12:52:47

VAX-11 Bliss-32 V4.0-742
DISK\$VMSMASTER:[PATCH.SRC]PATSPA.B32;1

Page 25
(7)

: ! End of module

.EXTRN LIB\$SIGNAL

PSECT SUMMARY

Name	Bytes	Attributes
-PAT\$OWN	12	NOVEC, WRT, RD ,NOEXE,NOSHR, LCL, REL, CON,NOPIC,ALIGN(2)
-PAT\$PLIT	116	NOVEC,NOWRT, RD ,NOEXE,NOSHR, LCL, REL, CON,NOPIC,ALIGN(0)
-PAT\$CODE	999	NOVEC,NOWRT, RD , EXE,NOSHR, LCL, REL, CON,NOPIC,ALIGN(2)
ABS .	0	NOVEC,NOWRT,NORD ,NOEXE,NOSHR, LCL, ABS, CON,NOPIC,ALIGN(0)

Library Statistics

File	----- Symbols -----			Pages Mapped	Processing Time
	Total	Loaded	Percent		
\$_\$255\$DUA28:[SYSLIB]LIB.L32;1	18619	31	0	1000	00:01.8

COMMAND QUALIFIERS

: BLISS/CHECK=(FIELD,INITIAL,OPTIMIZE)/VARIANT:1/LIS=LISS:PATSPA/OBJ=OBJ\$:PATSPA MSRC\$:PATSPA/UPDATE=(ENH\$:PATSPA)

: Size: 999 code + 128 data bytes
: Run Time: 00:34.2
: Elapsed Time: 02:04.8
: Lines/CPU Min: 3479
: Lexemes/CPU-Min: 37166
: Memory Used: 213 pages
: Compilation Complete

0303 AH-BT13A-SE
VAX/VMS V4.0

DIGITAL EQUIPMENT CORPORATION
CONFIDENTIAL AND PROPRIETARY

PATREB
LIS

PATSCA
LIS

PATSTO
LIS

PATRST
LIS

PATSPA
LIS

PATSSU
LIS